

## REMARKS

Claims 13-16 and 26-34 are pending. Claim 13 has been amended. Claims 26-34 have been added. No new matter has been added.

The Examiner objected to claims 14-16 as being dependent upon a rejected base claim, but stated that claims 14-16 would be allowable if rewritten in independent form. Claims 26, 27, and 31 recite the limitations of claims 14-16, respectively, including all of the limitations of the base claim and any intervening claims. Claims 28-30 directly depend from independent claim 26, and claims 32-34 directly depend from independent claim 31.

Independent claim 13, as amended, calls for a refresh circuit adapted to feed data received from the memory array back to the memory array to refresh the memory array. Support for independent claim 13, as amended, may be found in the specification at page 6, line 5 through page 7, line 2, for example.

Independent claim 13 stands rejected under 35 U.S.C. § 102(e) as being anticipated by Romero, et al., U.S. Patent No. 4,648,032 (hereinafter "Romero"). The Applicants request reconsideration and withdrawal of this rejection because Romero fails to teach or suggest a refresh circuit adapted to feed data received from the memory array back to the memory array to refresh the memory array.

Romero discloses a counter used to concurrently refresh a video display and a memory associated with such video display. Address bits are output from the counter to the sync decode logic, used to refresh the display, and to multiplexing means connected to the associated memory. Romero makes no mention of a refresh circuit adapted to feed data received from the memory array back to the memory array to refresh the memory array.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested.

Respectfully submitted,



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